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## PATENT ABSTRACTS OF JAPAN

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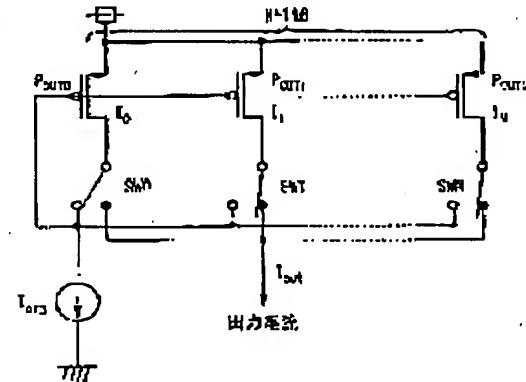
(21)Application number : 2001-251432 (71)Applicant : ASAHI KASEI MICROSYSTEMS KK  
 (22)Date of filing : 22.08.2001 (72)Inventor : YAMAHA YOSHIRO  
 TAKEHARA SATOSHI

## (54) DISPLAY PANEL DRIVE CIRCUIT

## (57)Abstract:

PROBLEM TO BE SOLVED: To reduce the variation in current.

SOLUTION: Transistors POUT0 to POUTn constitute a current mirror circuit with one transistor connected to a current source  $I_{ORG}$  to constitute a reference current source and other N-pieces transistors. Switching circuits SW0-SWN periodically switch over transistors constituting the reference current source, so that the outputs from other N-pieces transistors are derived as the drive output for a display panel. Since the time-division control (averaged by time) is performed for the current variation of all, N+1 pieces MOS transistors, the current variation can be suppressed.



## LEGAL STATUS

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